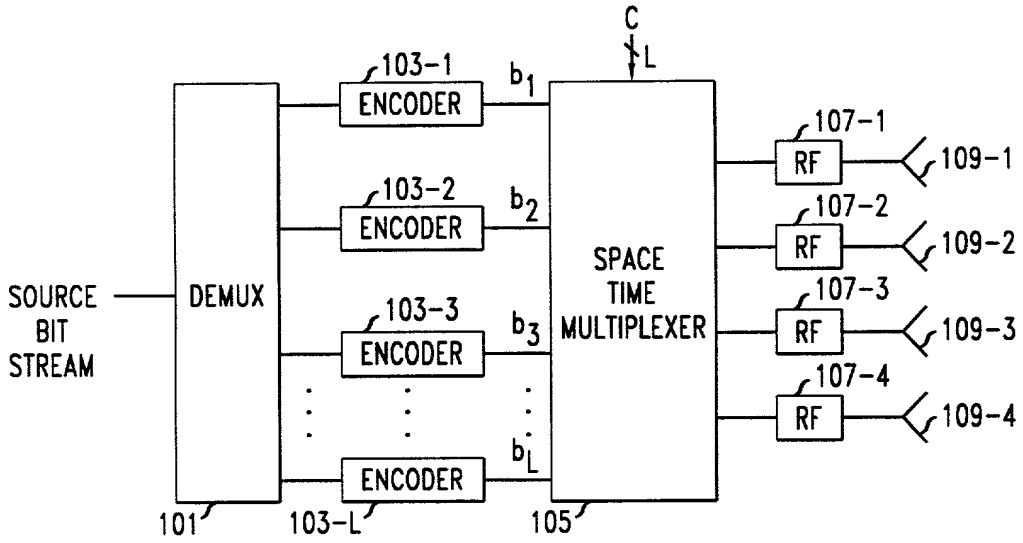
FIG. 1


C
L
103-1
ENCODER
b_1
103-2
ENCODER
b_2
103-3
ENCODER
b_3
ENCODER
b_L
SOURCE BIT STREAM
DEMUX
SPACE TIME MULTIPLEXER
107-1
RF
107-2
RF
107-3
RF
107-4
RF
109-1
109-2
109-3
109-4
101
103-L
105

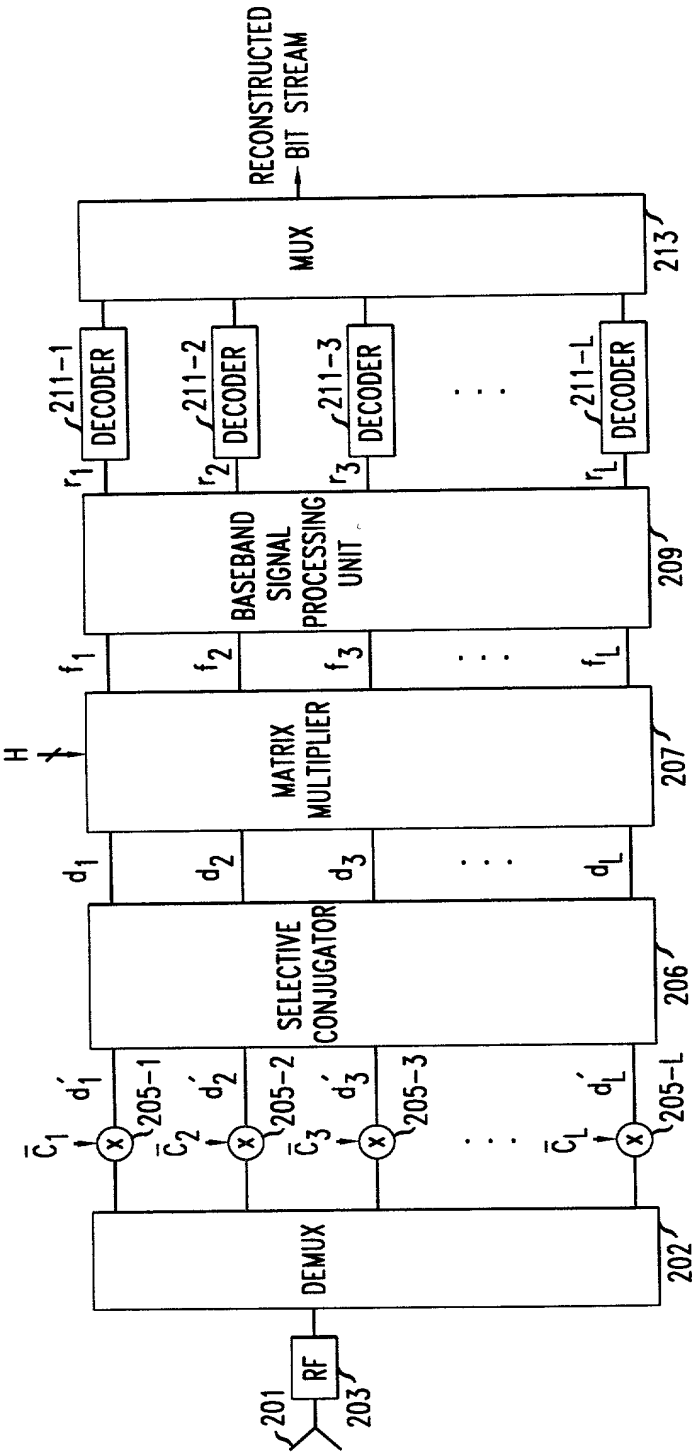

RECONSTRUCTED BIT STREAM
MUX
213
211-1
DECODER
211-2
DECODER
211-3
DECODER
211-L
DECODER
r_1
r_2
r_3
r_L
BASEBAND SIGNAL PROCESSING UNIT
209
f_1
f_2
f_3
f_L
H
MATRIX MULTIPLIER
207
d_1
d_2
d_3
d_L
SELECTIVE CONJUGATOR
206
$\bar{c}_1$
d'_1
205-1
$\bar{c}_2$
d'_2
205-2
$\bar{c}_3$
d'_3
205-3
$\bar{c}_L$
d'_L
205-L
DEMUX
202
RF
201
203

FIG. 2